



Atty. Dkt. No. 040373-0337

Applicant: Takahiro KOISHI
Title: INFORMATION PROCESSOR
Appl. No.: 10/625,565
Filing Date: 07/24/2003
Examiner: Beausoliel Jr., Robert W.
Art Unit: 2113

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of a document known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56.

A copy of each non-U.S. patent document and each non-patent document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The listed document is being submitted in compliance with 37 CFR §1.97(b), before the mailing date of the first Office Action on the merits, and within three (3) months of the mailing date of a communication from a foreign office.

RELEVANCE OF EACH DOCUMENT

The foregoing document was cited by the Examiner in Applicant's corresponding German application.

A translation of a portion of a German Office Action that issued September 19, 2005 with respect to a counterpart German application is provided below.

According to reference 1 (cf. Figure 1 with corresponding description) information processors are configured so as to comprise a CPU as well as input/output devices as well as I/O interfaces, wherein there are redundant interfaces and, in case of a fault, switching possibilities for the connection routes between the input/output devices and the interfaces. In the faultless state, data is received by the two interfaces and supplied by only one of the interfaces (cf. column 3, lines 41-51). When a fault occurs, the faulty interface is switched to passive so that data can only be sent and received via the normally operating interface (cf., e.g., column 7, line 62 to column 8, line 13). An English translation of the foreign-language document is not readily available. However, the absence of such translation does not relieve the PTO from its duty to consider the submitted foreign language document (37 CFR §1.98 and MPEP §609). An English language abstract is attached.

Applicant respectfully requests that each listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

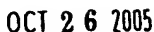
Respectfully submitted,

Date: October 26, 2005

FOLEY & LARDNER LLP
Customer Number: 22428
Telephone: (202) 672-5407
Facsimile: (202) 672-5399

By Thomas G. Bilodeau ^{Reg. No.} 43,438

for David A. Blumenthal
Attorney for Applicant
Registration No. 26,257



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1
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Application Number	10/625,565
Filing Date	07/24/2003
First Named Inventor	Takahiro KOISHI
Group Art Unit	2113
Examiner Name	Beausoliel Jr., Robert W.
Attorney Docket Number	040373-0337

[illegible][illegible]

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶

Date
Considered

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**